Call for Participation in the Ph.D. Forum at VLSI Design Conference 2016

The Ph.D. forum is a poster session for Ph.D. students to present and discuss their thesis and research work to a broad audience from both industry and academia in the relevant domain of VLSI design. The Ph.D. students may explore their research interests and career objectives with a panel of experts in the domain of VLSI design. The forum offers a good opportunity for students to receive valuable feedback and establish contacts for entering the job market. The forum also aims to develop a supportive community of research scholars and a spirit of collaborative research.

Eligibility
The following criteria define the eligibility for selection of a Ph.D. student in the Ph.D. forum:
(i) Students who have submitted their Ph.D. thesis within the last 12 months;
(ii) Students who are close to finishing their thesis work;
(iii) The topic of the dissertation must be within the scope of the conference as in the Call for Contributions on the conference website;
(iv) Previous forum presenters are not eligible.

Benefits
- A poster presentation at the Ph.D. forum
- Eligibility to win the Ph.D. forum award
- May distribute flyers summarizing the PhD work
- Possibility to apply for fellowship (partial travel grant) once selected.

Submission
A PDF for an extended abstract of the dissertation with a maximum limit of three A4 size pages in at least 11 pt, including figures, and bibliography (if applicable) along with name, affiliation, contact information, estimated (or actual) submission month/year. The three-page limit on the abstract will be strictly enforced. Please include a description of any supporting papers, including the publication forum. A list of all papers authored or co-authored by the student, related to the dissertation topic and included in the three-page abstract, will strengthen the submission.

Submit this material by email to phdforum.vlsidesign2016@gmail.com

Important dates
Submission deadline: **October 25, 2015**
October 31, 2015
Notification of acceptance: **November 10, 2015**
Presentation at VLSI Conference: To be announced in the conference program

Contact:
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