



# VLSID 2019

## The Go Green Conference



### Important dates:

**Submission of abstract:**  
7th December 2018

**Acceptance notification:**  
10 December 2018

**Final Presentation Due:**  
19 December 2018

**Conference:**  
7<sup>th</sup> - 9<sup>th</sup> January 2019

### CALL FOR USER/DESIGNER TRACK ABSTRACTS

32<sup>nd</sup> International Conference on VLSI Design and 18<sup>th</sup> International Conference on Embedded Systems is a forum for researchers and designers to present and discuss current topics in VLSI design, electronic design automation, embedded systems, and emerging technologies. The User/Designer Track at VLSID'19 invites abstract submissions from industry and academia in the following areas.

### TOPICS OF INTEREST

- Intelligence on Silicon
- Embedded Systems
- Security and Privacy
- IoT and Cyber-Physical Systems
- Digital Design
- Design Automation Algorithms
- Analog and Mixed Signal
- RF Design
- Power and Energy
- CMOS Technology and Devices
- Test and Verification
- Emerging Technologies

### SUBMISSIONS

All abstract submissions to the User/Designer Track should be made electronically via the conference website ([https://www.softconf.com/i/vlsid2019\\_user/](https://www.softconf.com/i/vlsid2019_user/)) by 7<sup>th</sup> December 2018. Your abstract should clearly state the novel ideas, results, and applications of the contribution. Submissions will undergo a double-blind review. Abstract must be in PDF format and not exceed 2 single-spaced pages including figures and references in two-column IEEE conference paper format (10pt font). Abstracts exceeding the page limit or identifying the authors will be rejected without review.